

A close-up, angled view of a microchip die, showing its intricate grid of circuitry and various colored regions (green, blue, red, yellow).

Welcome to

TEKMOS, INC

Simple Solutions for Complex Problems.



We design and manufacture a complete product line of the most cost effective technologies and combine them into highly integrated low cost devices including...

FPGAs, Microprocessors, Flash Memories, High Temperature Chips and ASICs.

TEST FLOOR GUIDELINES

Tekmos has an Anti-ESD Test Floor Area in compliance with ANSI/ESD S20.20.



ESD stands for Electro-Static Damage, Static Electricity Charge build up is everywhere and can pass from people to product in seconds and could damage the circuitry of the product, which leads to catastrophic or latent part defects.

- All visitors will be required to wear an ESD protected Smock, and be escorted on the Test Floor with an ESD trained employee.
- Please do not interfere with production process in any way and refrain from touching the material and machines.
- Photography and Video recordings are welcome, with permission from your escort.



In case of accident, injury, or potential Electro Static Damage, please notify your host immediately.

SIGN IN/SIGN OUT

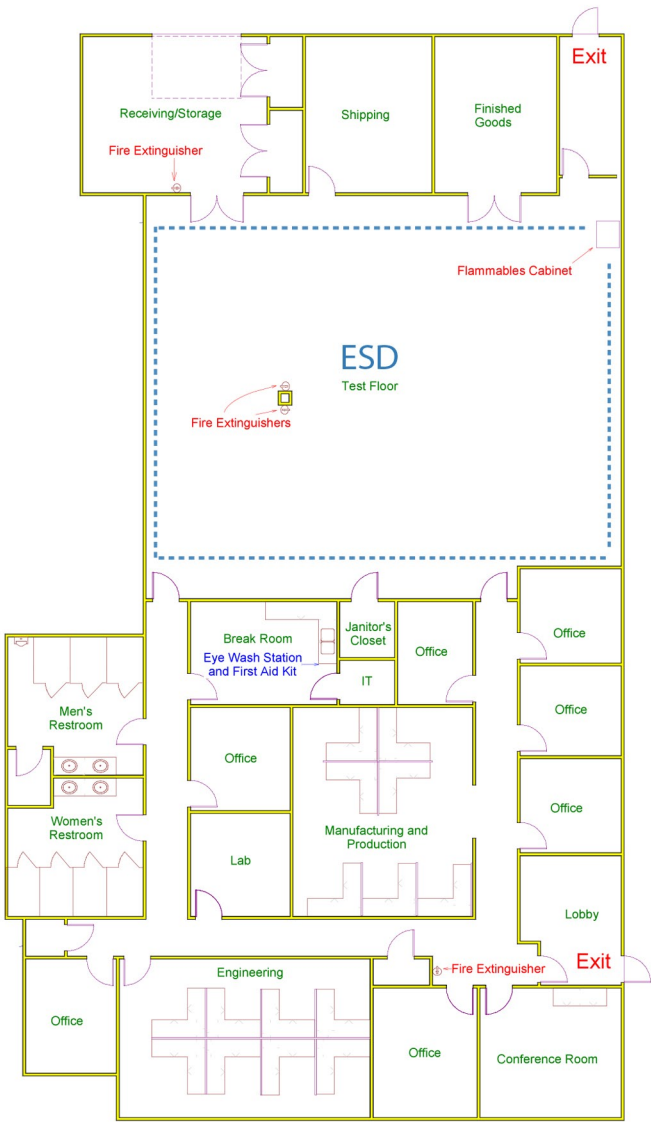
Please sign in and out of
the guest log in the front foyer.

!!! EVACUATION !!!

In case of evacuation, please use the front
or back exit doors identified on the Site Plan.

Have a great day visiting Tekmos!
For any questions or concerns,
please ask your host.

SITE PLAN



The Management Team



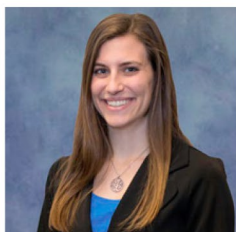
Lynn Reed, President, *Ext: 112*



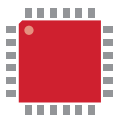
Bob Abrams, VP of Sales, *Ext: 114*



Jon Gehm, VP of Operation, *Ext: 125*



Kelsey Casto, Director of Quality, *Ext: 129*



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